

AMENDMENTS TO THE SPECIFICATION:

Page 1, kindly cancel the present title and substitute the following title:

MULTI-LEVEL HIERARCHICAL RADIO-FREQUENCY SYSTEM  
COMMUNICATION SYSTEM

Page 1, after the heading "CROSS REFERENCE TO RELATED APPLICATIONS," delete the following two paragraphs and substitute the following paragraphs:

CROSS REFERENCE TO RELATED APPLICATION

The present application is a continuation of Application No. 09/467,255, filed December 20, 1999, which is a division of Application No. 08/239,267 filed May 6, 1994, now U.S. Patent No. 6,006,100 issued December 21, 1999. Said application No. 08/239,267 is a continuation of Application No. 07/876,776 filed April 28, 1992, now abandoned, which is a continuation-in-part of Application U.S. application Serial No. 07/558,895[,] filed July 25, 1990, by Keenck et al. now abandoned. Said ApplicationThe application Serial No. 07/558,895 is a continuation-in-part of Application U.S. application Serial No. 07/529,353[,] filed May 25, 1990, now abandoned.

~~The present application~~ Said Application No. 07/876,776 is also a continuation-in-part of Application U.S. application Serial No. 07/854,115[,] filed March 18, 1992, by Keenck et al., which is a continuation-in-part of the above-mentioned Application U.S. application Serial No. 07/558,895[,] filed July 25, 1990.

On pages 6-9, kindly delete the paragraphs under the heading "SUMMARY OF THE INVENTION," and substitute the following new summary:

The present invention is useful in a multi-level digital data communication system. In such an environment, according one embodiment of the invention, at least one data communication device is provided. The data communication device preferably includes a microprocessor responsive to instructional codes arranged to process digital data messages having address codes and data codes and to route the data messages. A memory is arranged to store digital data messages coupled to the microprocessor. A first transceiver is arranged to communicate the data messages as first type radio frequency transmission signals, and a second transceiver is arranged to communicate the data messages as second type radio frequency transmission signals. The first and second transceivers are communicatively coupled to the microprocessor for selectively transmitting and receiving data messages to and from each of said first and second transceivers. Also provided is at least one first type data terminal device including a third transceiver arranged to communicate the data messages as the first type radio frequency transmission signals. The microprocessor processes data signals of data messages of communication between the at least one data communication device and the at least one first type data terminal device. Also provided is at least one data communication interface device including a fourth transceiver arranged to communicate the data messages at the second type radio frequency transmission signals, and a link arranged to communicate the data messages to a data station.